

i-spi

BUS-ADD [15:0]  
BUS-CLK  
BUS-RD  
BUS-RST  
BUS-WR  
EXT-START  
SDO  
SPI-CLK

BUS-DATA [7:0]  
SCLK  
SDI  
SEN  
SLD n/c

BUS-ADD [31:0]  
BUS-CLK  
BUS-RD  
BUS-RST  
BUS-WR

EXT-TRIGGER-ENABLE  
FIFO-READ  
TIMESTAMP-RESET  
TW-RESET  
TW-TRIGGER  
TRIGGER [7:0]  
TRIGGER-ACKNOWLEDGE  
TRIGGER-CLK  
TRIGGER-VETO [7:0]

i-tu-controller

BUS-DATA [7:0]  
FIFO-DATA [31:0]  
FIFO-EMPTY  
FIFO-PREEMPT-REQ  
TIMESTAMP [31:0] n/c  
TW-BUSY  
TW-CLOCK  
TW-ENABLED n/c  
TRIGGER-ACCEPTED-FLAG  
TRIGGER-ENABLED n/c  
TRIGGER-SELECTED [7:0]