

# command processor block

clk-40MHz-i  
 ctrl-command-block-from-ipbus-i [command-hifo-data][31:0]  
   [command-hifo-we]  
   [i2c-reset]  
   [i2c-reset-hifos]  
   [reply-hifo-read-next]

hybrid-settings-i[0][chips-enable][1:0]  
                                   [hybrid-enable]

ipb-clk-i  
 i2c-reply-i [cmd-data][127:0]  
                   [cmd-err]  
                   [cmd-strobe]

i2c-settings-map-i[0] [data-rd-n-bytes][4:0]  
                                   [data-wr-n-bytes][4:0]  
                                   [i2c-address][6:0]  
                                   31 [nack-en]  
                                   [register-address-nbytes][1:0]  
                                   [stop-for-rd-en]

mpa-ssa-board-reply-i[reply-word-0][31:0]  
 mpa-ssa-board-reply-i[strobe]  
 reset-i

ctrl-command-block-to-ipbus-o [reply-hifo-data][31:0]

i2c-request-o [cmd-broadcast]  
                   [cmd-chip-id][4:0]  
                   [cmd-data][127:0]  
                   [cmd-hybrid-id][3:0]  
                   [cmd-page]  
                   [cmd-read]  
                   [cmd-register][15:0]  
                   [cmd-strobe]

mpa-ssa-board-request-o [request-word-0][31:0]  
                                   [request-word-1][31:0]  
                                   [strobe]

stat-command-block-o [error-i2c-master][7:0]  
                           [hifo-statuses]