

- fast-signal {antenna-trigger}
 - [fast-reset]
 - [i2c-refresh]
 - [orbit-reset]
 - [test-pulse-trigger]
 - [trigger]
- stat-fastblock-o [error-code][7:0]
- stat-fastblock-o [if-configured]
 - [trigger-in-counter][31:0]
 - [trigger-source][3:0]
 - [trigger-state][1:0]
- tdc-value-o [7:0]

fst-command processor